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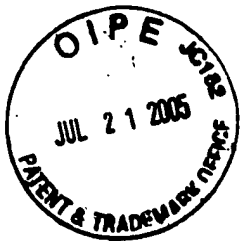
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TRANSMITTAL FORM (to be used for all correspondence during pendency of filed application)	Application Number	10/619,738	
	Filing Date	July 14, 2003	
	First Named Inventor	Sabhpathi Naidu Annamaneni	
	Group Art Unit Number	2812	
	Examiner Name	Not yet known	
Total Number of Pages in This Submission	2	Attorney Docket Number	22272-08030

ENCLOSURES (check all that apply)	
<input type="checkbox"/> Fee Transmittal Form (in duplicate) <input type="checkbox"/> Check Enclosed	<input type="checkbox"/> Issue Fee Transmittal
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REMARKS:	

SIGNATURE OF ATTORNEY OR AGENT			
Signature:			
Attorney/Reg. No.:	Michael W. Farn, Reg. No. 41,015	Dated:	JULY 19, 2005

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I hereby certify that this correspondence, including the enclosures identified above, is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date shown below. If the Express Mail Mailing Number is filled in below, then this correspondence is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service pursuant to 37 CFR 1.10.			
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PATENT

IN THE UNITED STATES

PATENT AND TRADEMARK OFFICE

APPLICANT(S): Sabhapathi Naidu Annamaneni and Sarangkumar Sharadchandra Bhavsar

APPLICATION NO.: 10/619,738

FILING DATE: July 14, 2003

TITLE: System and Method for Automating Integration of Semiconductor Work in Process Updates

EXAMINER: Not yet known

GROUP ART UNIT: 2812

ATTY. DKT. NO.: 22272-08030

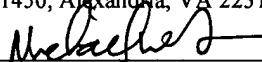
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Michael W. Farn, Reg. No. 41,015

COMMISSIONER FOR PATENTS
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STATUS REQUEST

SIR:

Our file for the subject application reveals that there has been no action on this application since the filing of our Information Disclosure Statement on January 20, 2005.


Please inform the undersigned, at the below stated address, of the status of this application.

Respectfully submitted,
SABHAPATHI NAIDU ANNAMANENI et al.

Dated:

July 19, 2005

By:


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